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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Tetsuya ISHIMARU et al.

Appln. No.: 10/743,783

Group Art Unit: 2824

Filed: December 24, 2003

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The documents on the attached List were cited in the specification, on pages 2, 3, 11 and 17, and their relevance is indicated therein.

Respectfully submitted,

MWS:lat

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April 27, 2003

By:

Mitchell W. Shapiro

Reg. No. 31,568

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